

Docket No.: 101188-88  
Customer No. 27387

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION NO. : PCT/JP2005/000097  
APPLICANT : Hiroo Sekiya  
FILED : January 7, 2005  
FOR : Circuit Design Support Method, Device Thereof, and Circuit Design  
Support Program  
ART UNIT : TBA  
EXAMINER : TBA

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July 6, 2006

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

**PRELIMINARY AMENDMENT**

SIR:

Prior to examination, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 5 of this paper.